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		Application Number	
		Filing Date	August 17, 2001
		First Named Inventor	Steven R. Jahnke, et al.
		Group Art Unit	2185
		Examiner Name	
Attorney Docket No.	TI-30523		

PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		Office ³	Number ⁴	Kind Code ² (if known)				
DM	BA	EP	0 432 799A		Nippon Electric Co.	6/19/1991		
DM	BB	WO	99 63447A		Advanced Micro Devices Inc.	12/9/1999		
	BC							
	BD							
	BE							
	BF							
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
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DM	CA	IBM Technical Disclosure Bulletin, <i>Direct Memory Access Controller</i> , Vol. 26, No. 10A, 1 march 1984, entire document.	
DM	CB	D. Flynn; AMBA: <i>Enabling Reusable On-Chip Designs</i> , IEEE Micro, IEEE Inc., New York, NY, US; Vol. 17, No. 4, 1 July 1997, entire document.	
	CC		
	CD		
	CE		
	CF		
	CG		
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Examiner Signature	<i>David Martinez</i>	Date Considered	9/16/03
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